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APPLICATION NO.	PLICATION NO. FILING DATE		FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.		CONFIRMATION NO.	
10/663.020 09/16/2003 Harold W. Chase YOR919980343US2 2534 FITLE OF INVENTION: METHOD FOR VLSI SYSTEM DEBUG AND TIMING ANALYSIS (12675Z)								
APPLN. TYPE	SMALL ENTITY	ISSUE F	SUE FEE PUBLICATION FEE		TOTAL FE	EE(S) DUE	DATE DUE	
nonprovisional	NO	\$1400	0	\$300	\$1	700	09/05/2006	
EXAMINER		ART UNIT		CLASS-SUBCLASS	7			
DIMYAN, MAGID Y			5 716-004000					
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